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L1	1521	703/2.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/10/11 16:58
L3	955	database and (logic near design)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/10/11 16:59
L4	564	L3 and @ad<"20010901"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/10/11 17:44
L5	30	("5187668" | "5212650" | "5313615" | "5432707" | "5452226" | "5487018" | "5519627" | "5541849" | "5623417" | "5666288" | "5696693" | "5699265" | "5726902" | "5727187" | "5757657" | "5761664" | "5764534" | "5818729" | "5841663" | "5864487" | "5956497" | "5960184" | "6026228" | "6080201" | "6145117" | "6154874" | "6216258" | "6263483" | "6289489" | "6308309").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/11 17:42
L6	227	hdl and (parameter$7 near design)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/10/11 17:43
L7	133	L6 and @ad<"20010901"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/10/11 17:44
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TD Givargis, F Vahid - HARDWARE SOFTWARE CODESIGN PROC INT WORKSHOP, 2000 -
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... generics, but rather may require new **HDL** code to ... appears as part of the SOC **design**,
such as ... Each configuration of a **parameterized** SOC architecture represents a ...
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or a **parameterized**-style IP ... of IP is an important issue in hardware **design**. ...
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... be **parameterized** without increasing **design** cost if the description is written in
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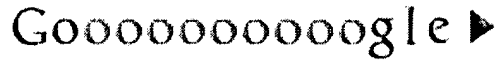Cited by 5 - Web Search - ieeexplore.ieee.org - csa.com

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☐   1. **On the fundamental design gap in terabit per second packet switching**
Verhappen, M.; van der Putten, P.H.A.; Voeten, J.P.M.;
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☐   **1. Advanced Analysis of Wearable Sensor Data to Adjust Medication Intake Parkinson¿s Disease**
Sherrill, D.M.; Hughes, R.; Salles, S.S.; Lie-Nemeth, T.; Akay, M.; Standaert, C
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☐   1. **IEEE standard computer dictionary. A compilation of IEEE standard com**
              IEEE Std 610
              18 Jan. 1991

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Sudo, T.; Ohtsuki, T.; Goto, S.;
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the promises of hardware synthesis is providing **logic design**ers with little or no experience in ASIC design
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can be factored out in the first step. PSM F-**logic design** step step impl. SQL3 ODMG-93 ?Figure 1:
Modelling Based Design of Object-oriented **Databases** Gyorgy Kov'acs Dept. of Information Systems
models (such as ER, NIAM, PSM) to object-oriented **databases**. This transformation is captured within the
ftp.cs.kun.nl/pub/SoftwEng.InfSyst/articles/OOformaltrafo.ps

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of Programs]H:2:1 [**Database** management]**Logic Design** H:2:3 **Database** management]Languages 1
Semantic Integration of Deductive **Databases** P. Asirelli IEI-CNR, Via S. Maria -Pisa,
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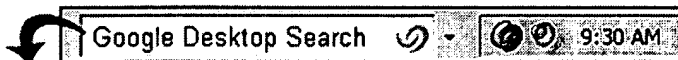File Format: PDF/Adobe Acrobat - View as HTML
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